

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	8248406
<b>Application Number:</b>	10587725
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	2259
<b>Title of Invention:</b>	Method for Manufacturing Soi Wafer
<b>First Named Inventor/Applicant Name:</b>	Etsuro Morita
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<b>Filer Authorized By:</b>	Anton E. Skaugset
<b>Attorney Docket Number:</b>	SHX 373
<b>Receipt Date:</b>	18-AUG-2010
<b>Filing Date:</b>	26-JUL-2006
<b>Time Stamp:</b>	22:01:16
<b>Application Type:</b>	U.S. National Stage under 35 USC 371

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	20100818_Third_Supplemental _IDS_SB08a-EFS_as- filed_SHX373.pdf	612512 07e25299a38295b3dd4491607f122621f75 4f2b8	no	4

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2	Foreign Reference	JP0411551_Suzuki_w-English-abs.pdf	888638 c09dc4fee70b467963281550ae23c2593689db8a	no	5
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3	Foreign Reference	JP04206766_Shigeniwa_w-English-abs.pdf	1326539 5c8d7ae1b819c707bac04e928028c6be926378cd	no	7
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4	Foreign Reference	WO93-20584_Gassel_etal_w-English-abs.pdf	4183441 18b0e51ff235f761fb4a0465982d61edbfcc641c	no	20
<b>Warnings:</b>					
<b>Information:</b>					
5	Foreign Reference	JP07226433_Hashimoto_w-English-abs.pdf	3441981 81cf99209ed40c042465e0557424de9fb82a04bd	no	13
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<b>Information:</b>					
6	NPL Documents	GASSEL_etal_SIMOX_and_Wafer_Bonding.pdf	1889710 5ea2914b6ace13934bf4a54d536aeb88f2b73840	no	2
<b>Warnings:</b>					
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7	NPL Documents	TONG_etal_VLSI_SOI_Fabrication_by_SIMOX_Wafer_Bonding.pdf	718360 fbbc3980d2d6f54804a4fba90145461deb722af	no	2
<b>Warnings:</b>					
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8	NPL Documents	YANG_etal_Silicon-on-Insulator-with-Active-Substrate.pdf	730908 1127ee236983aadf0dcbcd35b8ce4e8ca5855868	no	2
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